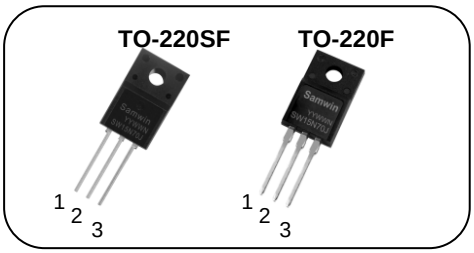
N-channel Enhanced mode TO-220SF/TO-220F MOSFET

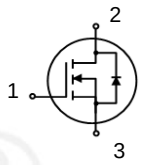
Features

- High ruggedness
- Low $R_{DS(ON)}$ (Typ 0.22Ω)@V_{GS}=10V
- Low Gate Charge (Typ 29nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application:LED , Charger, PC Power



1. Gate 2. Drain 3. Source

BV_{DSS} : 700V
I_D : 15A
$R_{DS(ON)}$: 0.22Ω



General Description

This power MOSFET is produced with advanced super junction technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.

Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW MN 15N70J	SW15N70J	TO-220SF	TUBE
2	SW F 15N70J	SW15N70J	TO-220F	TUBE

Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-220SF	TO-220F	
V_{DSS}	Drain to source voltage	700		V
I_D	Continuous drain current (@T_C=25°C)	15*		A
	Continuous drain current (@T_C=100°C)	9.5*		A
I_{DM}	Drain current pulsed (note 1)	45		A
V_{GS}	Gate to source voltage	±30		V
E_{AS}	Single pulsed avalanche energy (note 2)	360		mJ
E_{AR}	Repetitive avalanche energy (note 1)	36		mJ
dv/dt	MOSFET dv/dt ruggedness (@VDS=0~400V)	30		V/ns
dv/dt	Peak diode recovery dv/dt (note 3)	20		V/ns
P_D	Total power dissipation (@T_C=25°C)	44.6		W
	Derating factor above 25°C	0.36		W/°C
T_{STG}, T_J	Operating junction temperature & storage temperature	-55 ~ + 150		°C
T_L	Maximum lead temperature for soldering purpose, 1/8 from case for 5 seconds.	300		°C

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value		Unit
		TO-220SF	TO-220F	
R_{thjc}	Thermal resistance, Junction to case	2.8		°C/W
R_{thja}	Thermal resistance, Junction to ambient	50		°C/W

Electrical characteristic (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV_{DSS}	Drain to source breakdown voltage	V_{GS}=0V, I_D=250uA	700			V
ΔBV_{DSS} / ΔT_J	Breakdown voltage temperature coefficient	I_D=250uA, referenced to 25°C		0.71		V/°C
I_{DSS}	Drain to source leakage current	V_{DS}=700V, V_{GS}=0V			1	uA
		V_{DS}=560V, T_J=125°C			50	uA
I_{GSS}	Gate to source leakage current, forward	V_{GS}=30V, V_{DS}=0V			100	nA
	Gate to source leakage current, reverse	V_{GS}=-30V, V_{DS}=0V			-100	nA
On characteristics						
$V_{GS(TH)}$	Gate threshold voltage	V_{DS}=V_{GS}, I_D=250uA	2.5		4.5	V
$R_{DS(ON)}$	Drain to source on state resistance	V_{GS}=10V, I_D=7.5A,T_J=25°C		0.22	0.29	Ω
		V_{GS}=10V, I_D=7.5A,T_J=125°C		0.47		Ω
G_{fs}	Forward transconductance	V_{DS}=30V, I_D=7.5A		13		S
Dynamic characteristics						
C_{iss}	Input capacitance	V_{GS}=0V, V_{DS}=200V, f=1MHz		1251		pF
C_{oss}	Output capacitance			46		
C_{rss}	Reverse transfer capacitance			0.5		
$t_{d(on)}$	Turn on delay time	V_{DS}=325V, I_D=15A, R_G=10Ω, V_{GS}=10V (note 4,5)		21		ns
t_r	Rising time			31		
$t_{d(off)}$	Turn off delay time			60		
t_f	Fall time			28		
Q_g	Total gate charge	V_{DS}=520V, V_{GS}=10V, I_D=15A I_G=3mA (note 4,5)		29		nC
Q_{gs}	Gate-source charge			7		
Q_{gd}	Gate-drain charge			12		
R_g	Gate resistance	V_{DS}=0V, Scan F mode		11		Ω

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			15	A
I_{SM}	Pulsed source current				45	A
V_{SD}	Diode forward voltage drop.	I_S=15A, V_{GS}=0V			1.4	V
t_{rr}	Reverse recovery time	I_S=15A, V_{GS}=0V, dI_F/dt=100A/us		325		ns
Q_{rr}	Reverse recovery charge			4.3		uC

※. Notes
1. Repeatitive rating : pulse width limited by junction temperature.
2. L =60mH, I_{AS} =3.5A, V_{DD}=50V, R_G=25Ω, Starting T_J = 25°C
3. I_{SD} ≤ 15A, di/dt = 100A/us, V_{DD} ≤ BV_{DSS}, Staring T_J =25°C
4. Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%.
5. Essentially independent of operating temperature.

Fig. 1. On-state characteristics

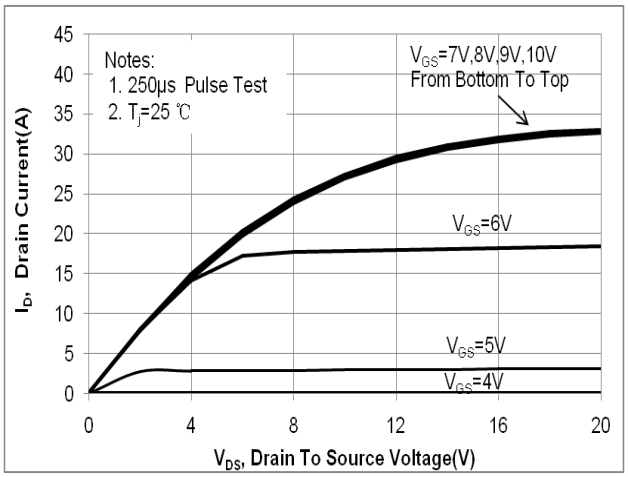


Fig. 2. Transfer Characteristics

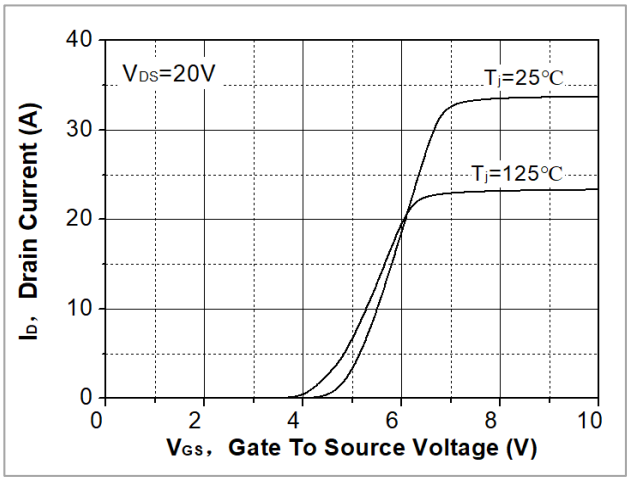


Fig. 3. On-resistance variation vs. drain current and gate voltage

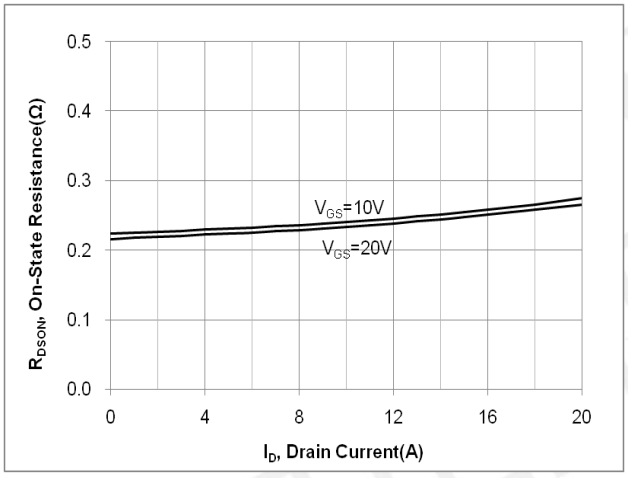


Fig. 4. On-state current vs. diode forward voltage

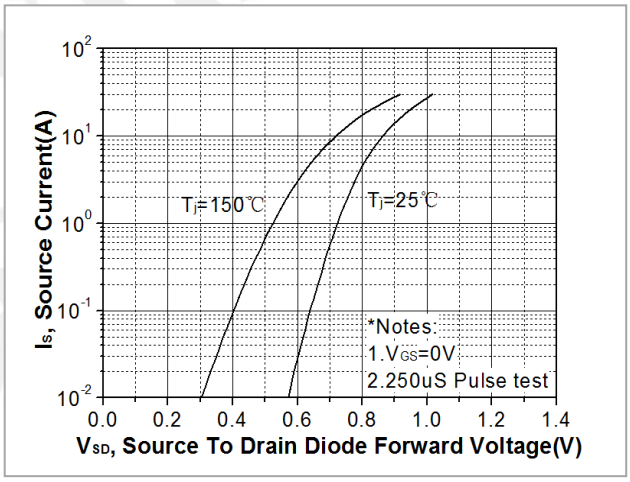


Fig 5. Breakdown voltage variation vs. junction temperature

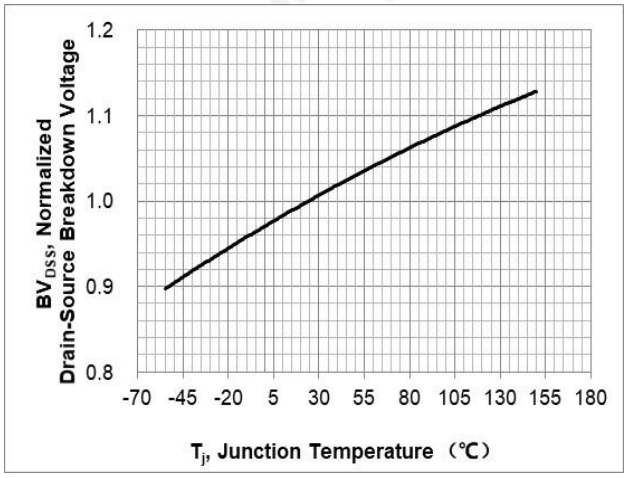


Fig. 6. On-resistance variation vs. junction temperature

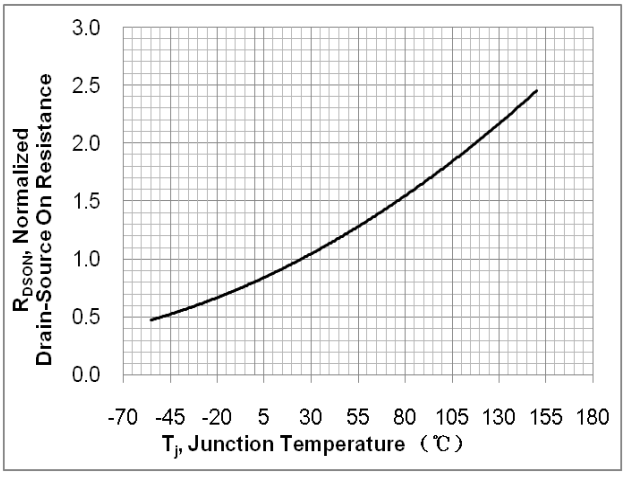


Fig. 7. Gate charge characteristics

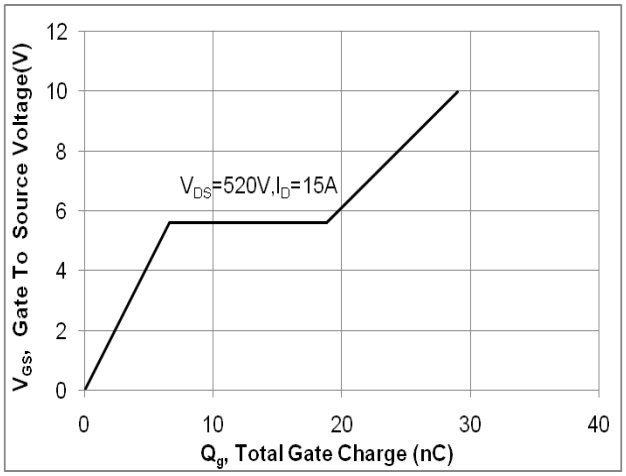


Fig. 8. Capacitance Characteristics

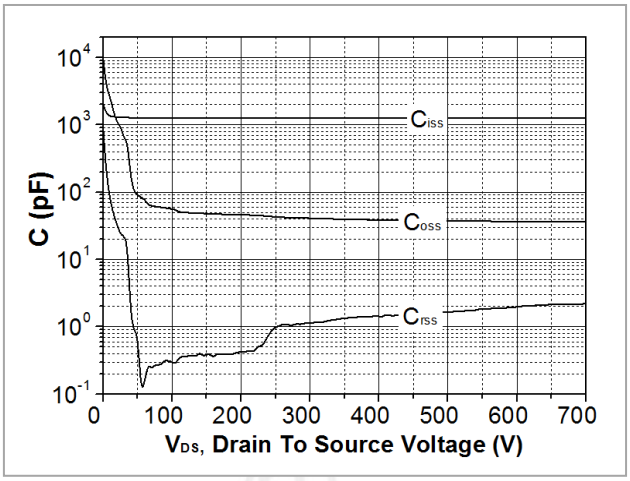


Fig. 9. Maximum safe operating area(TO-220SF/TO-220F)

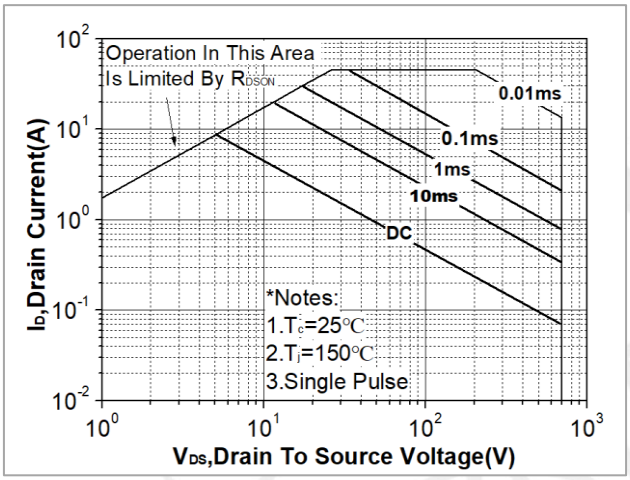


Fig. 10. Transient thermal response curve(TO-220SF/TO-220F)

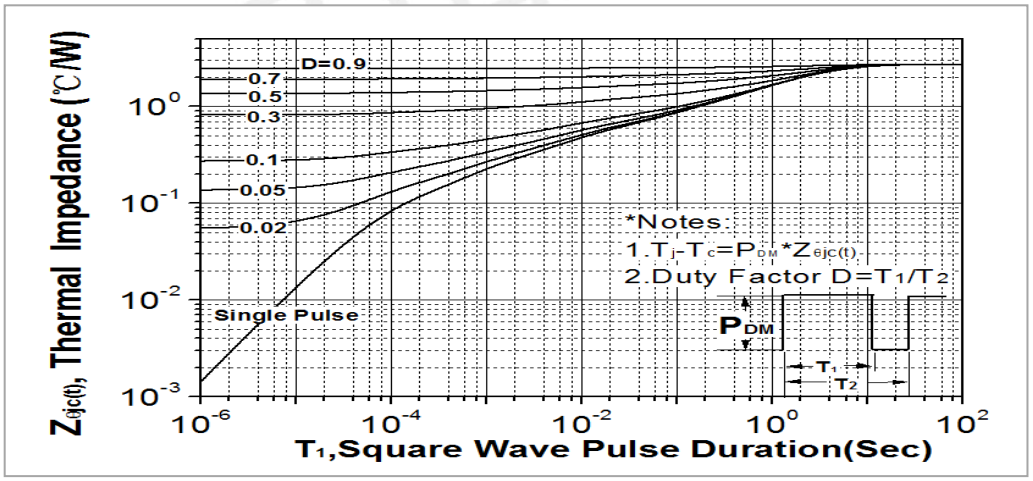


Fig. 11. Gate charge test circuit & waveform

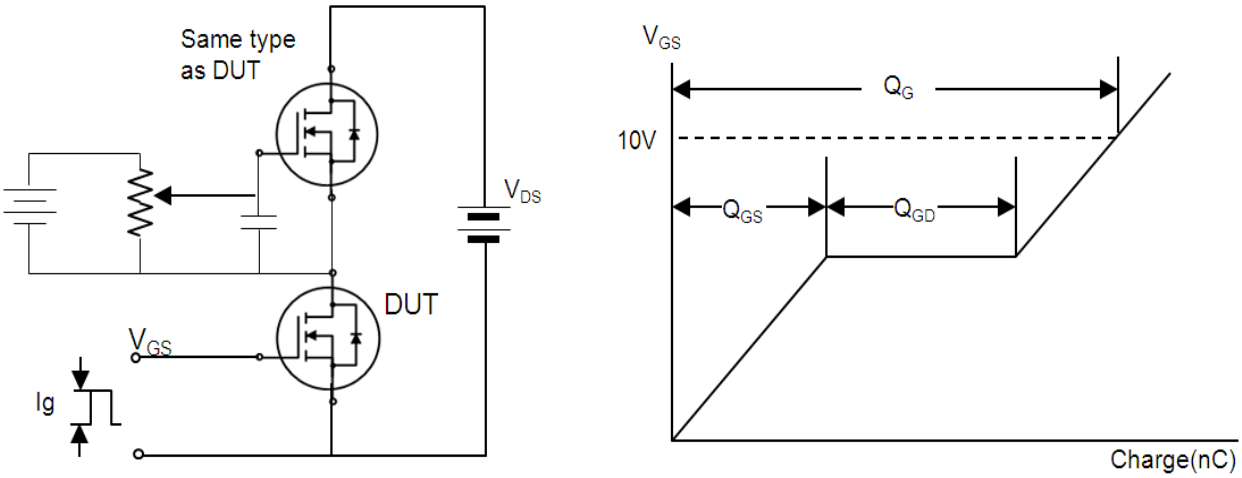


Fig. 12. Switching time test circuit & waveform

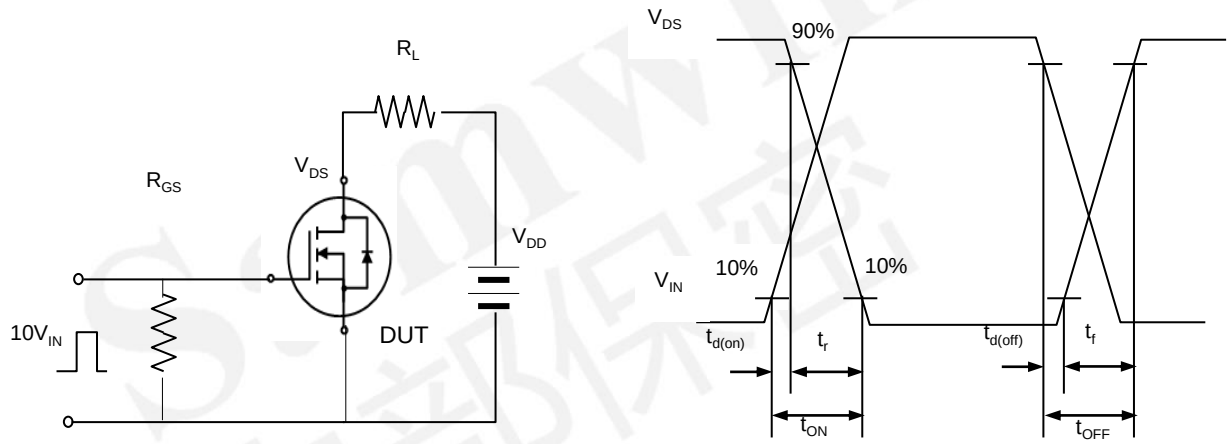
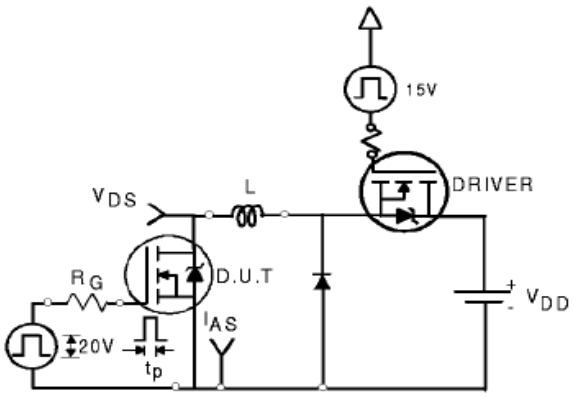


Fig. 13. Unclamped Inductive switching test circuit & waveform



$$E_{AS} = \frac{1}{2} L I_{AS}^2$$

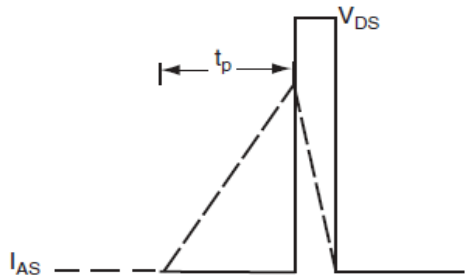
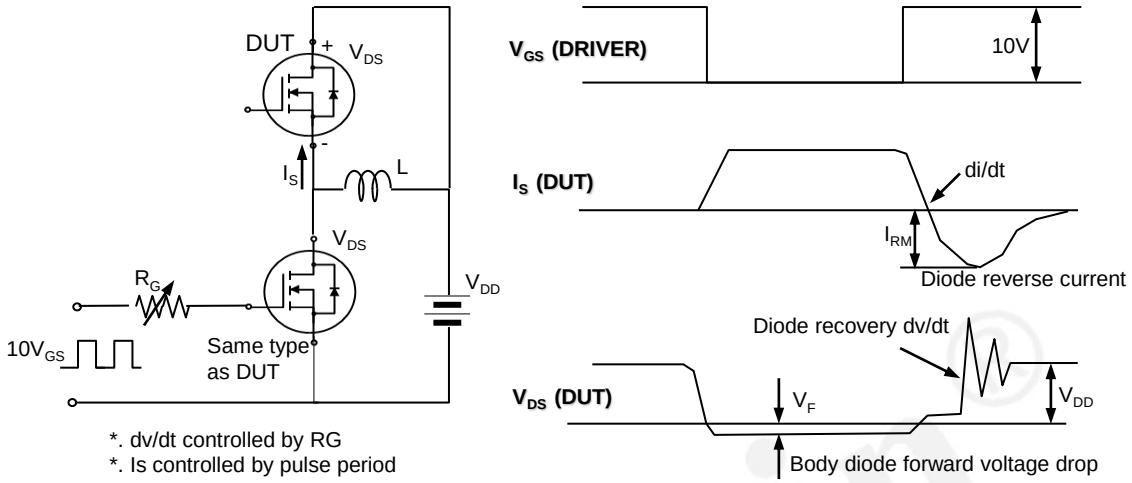


Fig. 14. Peak diode recovery dv/dt test circuit & waveform



DISCLAIMER

* All the data & curve in this document was tested in SEMIPOWER TESTING & APPLICATION CENTER.
* This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.
* Qualification standards can also be found on the Web site (**http://www.semipower.com.cn**)
* Suggestions for improvement are appreciated, Please send your suggestions to **samwin@samwinsemi.com**